

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(skew and buffer and (intermediate adj voltage) and (current adj source) and compensat\$3).clm.	US-PGPUB	OR	ON	2006/10/11 15:49
L2	1	(skew and buffer and (intermediate adj voltage) and (current adj source) and precharg\$3 and discharg\$3).clm.	US-PGPUB	OR	ON	2006/10/11 15:49
L3	0	(buffer and (intermediate adj voltage) and (current adj source) and (control adj terminal)).clm.	US-PGPUB	OR	ON	2006/10/11 15:50
L4	1	(buffer and (intermediate adj voltage) and (current adj source) and precharg\$3).clm.	US-PGPUB	OR	ON	2006/10/11 15:50
L5	5	(buffer and differential and (intermediate adj voltage) and (current adj mirror)).clm.	US-PGPUB	OR	ON	2006/10/11 15:50
L6	1	(memory and buffer and (intermediate adj voltage) and (current adj source)).clm.	US-PGPUB	OR	ON	2006/10/11 15:50
L7	1	(buffer and differential and (intermediate adj voltage) and inverter and mirror and drive).clm.	US-PGPUB	OR	ON	2006/10/11 15:51
L8	2	(buffer and differential and (intermediate adj voltage) and inverter and mirror).clm.	US-PGPUB	OR	ON	2006/10/11 15:51
L9	16	(skew and buffer and (intermediate adj voltage) and (current adj source) and compensat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:49
L10	1	(skew and buffer and (intermediate adj voltage) and (current adj source) and precharg\$3 and discharg\$3).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:49
L11	6	(skew and buffer and (intermediate adj voltage) and (current adj source) and precharg\$3 and discharg\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:49

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L12	91	(buffer and (intermediate adj voltage) and (current adj source) and (control adj terminal))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:50
L13	226	(buffer and (intermediate adj voltage) and (current adj source) and precharg\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:50
L14	301	(buffer and differential and (intermediate adj voltage) and (current adj mirror))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:50
L15	419	(memory and buffer and (intermediate adj voltage) and (current adj source))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:50
L16	201	(buffer and differential and (intermediate adj voltage) and inverter and mirror and drive)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:51
L17	253	(buffer and differential and (intermediate adj voltage) and inverter and mirror)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:51
L18	632	10 or 11 or 12 or 13 or 14 or 15 or 16 or 17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 15:52